

No	Terminal Name	I/O	Description	P.OFF	P.Failure	Reset/ Release
58	LED Rch OUT	O	LED ON/OFF control terminal. "High": ON "Low": OFF	Normal Operation	Low	Low
59	LED PLAY OUT	O	LED ON/OFF control terminal. "High": ON "Low": OFF	Normal Operation	Low	Low
60	VCR/TV	O	TV/VTR output selection control signal * P.OFF, Shifting to T,Standby: "Low" output. * Shifting to VV from EE: "High" output. * The condition is inverted whenever the TV/VCR Key is received.	Low	Low	Low
61	PAL 9H	O	PAL 9H: High is output.	Low	Low	Low
62	NC	O	Low fix.	Low	Low	Low
63	LED_Lch_OUT	O	LED ON/OFF control terminal. "High": ON "Low": OFF	Normal Operation	Low	Low
64	NC	O	Low fix.	Low	Low	Low
65	LED_P_ON	O	LED ON/OFF control terminal. "High": ON "Low": OFF	Normal Operation	Low	Low
66	UNLOADING(H)	O	Control terminal for the Unloading operation.	Low	Low	Low
67	LOADING(H)	O	Control terminal for the loading operation.	Low	Low	Low
68	IC.DATA.OUT	O	Timer-Bus signal for peripheral ICs control: Data output	(Normal ope.)	In	Hi-Z
69	IC.DATA.IN	I	Timer-Bus signal for peripheral ICs control: Data input	(Normal ope.)	In	In
70	IC.DATA.CLK	O	Timer-Bus signal for peripheral ICs control:Clock outout	(Normal ope.)	In	Hi-Z
71	IIC.CLK	O	Serial communication terminal (IIC) for IC3001/FM audio IC.	(Normal ope.)	In	Hi-Z
72	IIC.DATA	I/O	Serial communication terminal (IIC) for IC3001/FM audio IC.	(Normal ope.)	In	Hi-Z
73	125Hz/ROM.CORE	O	ROM Correction confirmation mode: * ROM correction setting bit is "ON": "High". * ROM correction setting bit is "OFF": "Low" is existed. (Other than ROM Correction confirmation mode, this terminal is the output terminal of internal clock for main clock adjustment.:Outputting the 125Hz.)	(Normal ope.)	Low	(Normal ope.)
74	CAP R/F	O	The rotation direction control terminal of the capstan driver. *RVS="High" *FWD="Low".	Low	Low	Low
75	LED_LEFT	O	LED ON/OFF control terminal. "High": ON "Low": OFF	Normal Operation	Low	Low
76	CAP.ET	O	Power supply terminal for the capstan motor control. (Compared with the driver reference voltage, when it is "low", current will be cut. Also when it is "high", the rotation speed will be accelerated.)	High PWM=0V	Low	High PWM=0V
77	CYL.ET	O	Power supply terminal of the cylinder motor control: (Compared with the driver reference voltage, when it is "high", current will be cut. Also when it is "low", the rotation speed will be accelerated. (Max:2.8V))	Low PWM=2.800V	Low	Low PWM=2.800V
78	LED_STBY_REC	O	LED ON/OFF control terminal. "High": ON "Low": OFF	Normal Operation	Low	Low
79	S.REEL.PULSE	I	Input terminal of the S.Reel pulse.	In	In	In
80	T.REEL.PULSE	I	Input terminal of the T.Reel pulse.	In	In	In
81	BIAS(L)	O	Linear Audio REC/ERASE ON/OFF control terminal. (FULL ERASE ON/OFF control is combined with) *When recording the linear audio, the "Low" is output synchronizing with D.REC signal for IC3001. Output mode: REC(ADUB/AV-INSERT) *When the recording starts, "Low" is output at 140-160m sec. after D.REC (H) for IC3001 has been shifted to "High" from "Low". *When the recording stops, the "High" is output at 0-20m sec. after D.REC for IC3001 has being shifted to "low" from "High".	In	In	In